

	FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. LT-168	SERIAL NO. 10/761,502
	INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT Eddleman et al.	
			FILING DATE January 20, 2004	GROUP 2819 2816

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIALS	
JZ	Galinski, Martin; "Circuit manages power-up sequencing"; EDN; October 31, 2002.
JZ	Linear Technology; "LT1645 Dual-Channel Hot Swap Controller/Power Sequencer"; Datasheet; 1999.
JZ	Linear Technology; "LTC2920-1/LTC2920-2 Single/Dual Power Supply Margining Controller"; Datasheet, Initial Release; March 2003.
JZ	Linear Technology; "LTC3205 Multidisplay LED Controller"; Datasheet, Initial Release; August 2003.
JZ	Maxim Integrated Products; "MAX5039/MAX5040 Voltage-Tracking Controllers for PowerPC, DSPs, and ASICs"; Datasheet; May 2002.
JZ	Summit Microelectronics, Inc.; "SMT4004 QUAD TRAKKING™ POWER SUPPLY MANAGER", Datasheet; June 9, 2003.
JZ	Summit Microelectronics, Inc.; "SMT4004 QUAD TRACKING POWER SUPPLY MANAGER ADVANCED CURRENT SENSING SCHEMES AND POWER MOSFET SELECTION"; Application Note 20; February 21, 2002.
JZ	Summit Microelectronics, Inc.; "SMT4004 QUAD TRACKING POWER SUPPLY MANAGER Windows GUI Users Guide and Configuration Register Descriptions"; Application Note 22; August 23, 2002.
JZ	Summit Microelectronics, Inc.; "SMT4004-Advanced Voltage Tracking Methods Boost Efficiency, Reliability"; Application Note 26, Advance Information; October 16, 2002.
JZ	Summit Microelectronics, Inc.; "Xilinx Virtex™-E, Spartan™-IIE FPGA and SMT4004 TRAKKER™ Supply Manager Reference Design: Procedure and Results Summary"; Application Note 31; January 7, 2003.
JZ	Summit Microelectronics, Inc.; "Lossless Tracking Procedure and Results Summary Reference Design: Xilinx Virtex™-E, Spartan™-IIE FPGA and SMT4004 TRAKKER™"; Application Note 34; January 7, 2003.

EXAMINER Jeffrey Zweig

DATE CONSIDERED 1/3/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.